

Career Objective

Optimist to work with an organization where professionalism and enthusiasm are recognized and continuously acquire skills in emerging trends to achieve a top position while being resourceful, innovative and flexible.

SUSMITA PAUL



Skill Highlights

- Project management
- Good in Mat Lab, LATEX, Microsoft Word, Excel, PowerPoint etc
- Strong decision maker
- Good Communication Skill
- Service-focused

Education

School Education :

Name of the School	Name of examinations	Name of the Board	Subjects	Year of Passing	Percentage of marks	Division Secured
M.T.GIRLS' H.S. School	Madhyamik (Secondary)	T.B.S.E	All Subjects	2008	68.57%	I
	Higher Secondary(+2 Stage)	T.B.S.E	Physics, Chemistry, Math, English, Bengali	2010	67.6%	I

Graduation:

Name of the College	Name of the Course	Name of the University	Subjects	Year of Passing	Percentage of marks	Grade
TIT, Narsingarh	B.E.(Electrical Engineering)	Tripura University	All Subjects of B.E.(Electrical Engineering)	2014	79.8%	B

Post Graduation:

Name of the College/University	Name of the Course	Subjects	Year of Passing	Percentage of marks	Grade
Tripura University	M.TECH.(Electrical Engineering)	All Subjects of M.TECH.(Electrical Engineering)	2016	80.45%	A
NIT Agartala	PhD(Persuing)	Electrical Engineering	-	-	

Contact

Address:

Jahaharimura, Near Saroj Sangha Cub, Agartala, Tripura
PO:Agartala College,
PIN: 799004

Phone:

7005425730

Email:

er.susmitapaultit@gmail.com

Languages known:

English, Bengali and Hindi

Academic Projects:

Project B.E

Title : Minimization of loss in ac transmission line.

Period : August 2013-November 2013

Organization : Tripura Institute of Technology, Narsingarh.

Project M.TECH

Title : Controlling UPFC by Full order State Feedback Controller

Period : June 2015-June 2016

Organization : Tripura University.

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Working Experience:

I am working as an Asst. Professor in Electrical Engg. Dept. of Techno College of Engineering, Agartala for last 7 years.

Seminars & Workshops attended:

- BASICS OF PHOTONICS on 1st February 2014.
- Two days' workshop on Advances in Manufacturing Technology and Mechatronic Systems, March (13-14), 2015.
- Workshop on SMART GRID on 27th April, 2015.
- Two Days Workshop on Virtual Laboratory on 13-14th January, 2016.
- 2-Day Familiarization Workshop on Nanofabrication Technologies on February 15-16, 2016.
- Workshop on Photonics, Electronics, Nano Technology, Integrated Circuits and Systems on February 29-March 4, 2016, etc.

FDPs & NPTEL courses attended:

- 5 Days Faculty Development Program on Technological Advancements in Electric Vehicle Applications
- 5 Day Online Faculty Development Program based on "RECENT TRENDS IN MATERIALS AND FLEXIBLE ELECTRONICS"
- Faculty Development Programme (FDP) on Reforms in Examination System in perspective of National Education Policy 2020.
- 12 weeks NPTEL course on Control System Engineering.

Research Paper:

- 'Controlling SSSC by Full Order State Feedback Controller', International Journal of Computer Applications (0975-8887), Volume 151-No.9, October 2016.
- 'Second Harmonic Generation in a Parallel Slab With 1-D Photonic Crystal Multilayering', 979-8-3315-4082-1/24/\$31.00 ©2024 IEEE.

Personal Details:

Husband's name	: Prasenjit Purakayastha
Gender	: Female
Date of Birth	: 28th November 1992
Nationality	: Indian
Languages Known	: English, Bengali and Hindi

I hereby declare that all the information furnished above is true to the best of my Knowledge.

Dated:

Susmita Paul